Electronic Acknowledgement Receipt			
EFS ID:	1908258		
Application Number:	10765631		
International Application Number:			
Confirmation Number:	2270		
Title of Invention:	Verilog to C++ language translator		
First Named Inventor/Applicant Name:	Ghanashyam A. Bailwal		
Customer Number:	23446		
Filer:	Mirut P. Dalal		
Filer Authorized By:			
Attorney Docket Number:	15398US01		
Receipt Date:	25-JUN-2007		
Filing Date:	27-JAN-2004		
Time Stamp:	18:13:45		
Application Type:	Utility		
Payment information:			

Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1	Amendment - After Non-Final Rejection	ResponsetoOfficeAction1539 8US01.pdf	37352	no	14
Warnings:					

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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

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If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in the course

New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the international Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.